PTO/SB/17 (12-04v2)
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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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101	Effective on 12/08/20	Complete if Known					
Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818).			Application Number Pa		Patent#: 6,918,072		
FEE TRANSMITTAL			Filing Date	ls	Issued: July 12, 2005		
	For FY 200	First Named Inv	ventor T	Timothy B. Cowles			
	FOIT I ZU	Examiner Name		D. Ton			
Applicant	claims small entity status	Art Unit	2	2133			
TOTAL AMOUN	T OF PAYMENT	Attorney Docket	No. N	M4065.0559/P559-A			
METHOD OF PAYMENT (check all that apply)							
Check X Credit Card Money Order None Other (please identify):							
Deposit Account Deposit Account Number: 04-1073 Deposit Account Name: Dickstein Shapiro Morin & Oshinsky LLP							
For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)							
X Charge fee(s) indicated below Charge fee(s) indicated below, except for the filing fee							
Charge any additional fee(s) or underpayment of X Credit any overpayments							
fee(s) under 37 CFR 1.16 and 1.17							
FEE CALCULATION							
1. BASIC FILING, SEARCH, AND EXAMINATION FEES							
	FILI	NG FEES SE. Small Entity	ARCH FEES Small Entity	EXAMIN	ATION FEES Small Entity	•	
Application Ty	pe Fee (\$)	Fee (\$) Fee (\$		Fee (\$)	Fee (\$)	Fees Pai	d (\$)
Utility	300	150 500	250	200	100		
Design	200	100 100	50	130	65		
Plant	200	100 300	150	160	80		
Reissue	300	150 500	250	600	300		
Provisional	200	100 0	0	0	0		
2. EXCESS CLAIM FEES Small Entity							
Fee Description							Fee (\$)
Each claim over 20 (including Reissues)							25
Each independent claim over 3 (including Reissues) 200 100							
Multiple dependent claims						360	180
Total Claims	tal Claims		Paid (\$) <u>Mu</u>		ultiple Dependent Claims		
	- = x	=		Fee	<u>: (\$)</u>	Fee Paid (\$)	
Indep. Claims	Extra Claims	Fee (\$) Fee	Paid (\$)				
= × = 3. APPLICATION SIZE FEE							
If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).							
Total Sheets			additional 50 or fra		Fee (\$)	<u>Fee Pa</u>	id (\$)
100 = /50 (round up to a whole number) x =							
4. OTHER FEE(S) Fees Paid (\$)							
Non-English Specification, \$130 fee (no small entity discount)							
Other (e.g., late filing surcharge): 1811 Certificate of correction 100.00							
SUBMITTED BY							
Signature	Registr (Attorne			28,371	Telephone	(202) 828-	2232
Name (Print/Type)	Thomas J. D'Amico)			Date	December 1	, 2005





Docket No.: M4065.0559/P559-A

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:

Timothy B. Cowles, et al.

Patent No.: 6,918,072

Issued: July 12, 2005

For: CIRCUIT AND METHOD FOR TIME-

EFFICIENT MEMORY REPAIR

REQUEST FOR CERTIFICATE OF CORRECTION PURSUANT TO 37 CFR 1.322 & 1.323

Attention: Certificate of Correction Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentees noted typographical errors which should be corrected.

In the Specification, Applicants made the following errors that should be corrected:

Column 1, line 55, "256 chip" should read --256 chips--;

Column 13, line 2, "address" should read --addresses--; and

Column 14, line 39, "illustrates" should read --illustrate--.

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Patent No.: 6,918,072 Docket No.: M4065.0559/P559-A

In the Claims, the PTO made the following errors that should be corrected:

Claim 55, column 21, line 18, "redundant, planes" should read --redundant planes--; and

Claim 68, column 22, line 31, "scoring" should read --storing--.

In the Other Publications portion of the References Cited section the PTO made the following errors that should be corrected:

"Micron Technology, Inc.; "Synchronous DRAM" Data Sheets; 128Mb: x32 SDRAM: x32 DRAM, 128MbSDRAMx32, p65—Rev. Sep. 2000, pp. 1-52."

Should Read

--Micron Technology, Inc.; "Synchronous DRAM" Data Sheets; 128Mb: x32 SDRAM, 128Mb: x32 DRAM, 128MbSDRAMx32, p65 - Rev. Sep. 2000, pp. 1-52.--.

The errors were both found in the application as filed by Applicants and made by the PTO. Please charge our Credit Card in the amount of \$100.00 covering the fee set forth in 37 CFR 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

The errors now sought to be corrected are inadvertent typographical errors the correction of which does not involve new matter or require reexamination.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Patent No.: 6,918,072 Docket No.: M4065.0559/P559-A

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0677/P677-A.

Dated: December 1, 2005

Respectfully submitted,

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DEC

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page _1_ of _1_

PATENT NO.

6,918,072

APPLICATION NO.

09/864,682

ISSUE DATE

July 12, 2005

INVENTOR(S)

Timothy B. Cowles, et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification, the following errors are corrected:

Column 1, line 55, "256 chip" should read --256 chips--;

Column 13, line 2, "address" should read --addresses--; and

Column 14, line 39, "illustrates" should read --illustrate--.

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